IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Tomohiro SAKAI

Serial No.:

Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date:

Concurrently Herewith

Examiner:

Unknown

For:

METHOD AND SYSTEM FOR ANALYZING LOOP INTERFACE

FAILURE

Honorable Commissioner of Patents Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), the relevance of some of these documents is discussed on page 2 of the subject application. Further, English-language Abstracts are attached to the references.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Registration No. 34,386

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Customer No. 21254

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INFORMATION DISCLOSURE CITATION					Applicant(s)	<u></u>	Not yet Assigned			
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				U.S. PAT	ENT DOCUMENTS					
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	REF	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	Transl YES	slation NO	
		2001-167039	06/22/2001	Japan				ABS		
		2001-216206	0/10/2001	Japan				ABS		
		2002-7077	01/11/2002	Japan				ABS		
		2000-347812	12/15/2000	Japan			+	ABS		
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Form PTO-A820 (also form PTO-1449) P09A/REV04

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